

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	FINFET WITH LOW GATE CAPACITANCE AND LOW EXTRINSIC RESISTANCE	
Application Number :		
Date :		
First Named Applicant:	Brent A. Anderson	
Confirmation Number:		
Attorney Docket Number:	BUR920040040US1	
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Submitted By:		Elec. Sign.
William D. Sabo		/William D. SABO, Reg 27465/
		Sign. Capacity Attorney

Documents being submitted:	Files
us-assignment	BUR920040040US1-usassn.xml us-assignment.xsl us-assignment.dtd BUR920040040US1Assign1.tif BUR920040040US1Assign2.tif
us-request	BUR920040040US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	BUR920040040US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-ids	BUR920040040US1-usidst.xml us-ids.dtd us-ids.xsl
us-declaration	BUR920040040US1Dec1.tif
us-declaration	BUR920040040US1Dec2.tif
application-body	BUR920040040US1.xml application-body.dtd Image1.tif Image2.tif Image3.tif Image4.tif Image5.tif Image6.tif Image7.tif Image8.tif isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isogr1.ent isogr2.ent isogr3.ent isogr4.ent isolat1.ent isolat2.ent isomfrk.ent isomopf.ent isomscr.ent isonum.ent isopub.ent isotech.ent mathml2.dtd

	mathml2-qname-1.mod
	mmlalias.ent
	mmlextra.ent
	soextblx.dtd
	us-application-body.xsl
	wipo.ent
application-body-pdf-wrap	BUR920040040US1-pdf-wrap.xml
abstract-pdf	BUR920040040US1-abst.pdf
claims-pdf	BUR920040040US1-clms.pdf
description-pdf	BUR920040040US1-desc.pdf
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Comments	
Assignee Address: International Business Machines Corporation, Armonk, NY 10504	